

AMENDMENTS TO THE SPECIFICATION:

Please amend paragraph [0004] as follows:

[0001] IHS's which feature bus mastering components present significant hurdles to achieving C3 and higher power savings states because any bus mastering activity prevents processors from entering C3 and higher states. One example of a type of component which utilizes bus mastering is a Universal Serial Bus (USB) component. USB component's prevalence in modern IHS's presents an acute challenge in designing more power efficient IHS's.